

REMARKS

Claims 1-17 were pending in the above-identified application. The present amendment amends claims 1, 3, 4, 6, 9, 11, 13, 15 and 17 and cancels, without prejudice, claims 2, 10 and 16. Thus, claims 1, 3-9, 11-15 and 17 are presently pending.

Applicants respectfully request that the attorney docket number of record be changed. In particular, Applicants respectfully request replacing "RONI005/00US" with --16102US01--. Any courtesies extended in this respect would be gratefully appreciated.

In the Office Action dated March 25, 2004, claims 1-17 were rejected 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,624,767 B1 ("the Shiu patent"). Applicants respectfully traverse the rejection as set forth below.

Independent claim 1 has been amended to include some elements that were previously recited in dependent claim 3 (but which now have been deleted from claim 3). Thus, claim 1 now recites "wherein said memory buffer stores the data, and wherein said means performs said second de-interleaving as the stored data is read from said memory buffer".

To anticipate claim 1, the Shiu patent must describe each and every element as set forth in claim 1. Applicants respectfully submit that the Shiu patent does not describe, for example, "wherein said means performs said *second de-interleaving* as the stored data is *read* from said memory buffer" (italics added for emphasis). As is clear from the sections of the Shiu patent cited in the Office Action, the Shiu patent describes that a "[s]*second de-interleaving 252* can be achieved at the receiver unit by *writing* the received symbols for each physical channel to the memory bank *in a permuted order complementary to that used at the transmitter unit to achieve the second interleaving*" (italics added for emphasis). Col. 9, lines 22-26 of the Shiu patent. Further evidence in support of Applicants' contention can be found, for example at col. 13, lines 61-64 of the Shiu patent: "Specifically, the *second de-interleaving 252* can be achieved by *writing* the symbols to buffer 512 *in a second permuted order that is complementary to that performed for the second interleaving 238*" (italics added for emphasis). Thus, Applicants respectfully submit that the Shiu patent does not describe "wherein said means performs said second de-interleaving as the stored data is read from said memory buffer". Since the Shiu patent does not describe each and every element as set forth in claim 1, the Shiu patent does not anticipate claim 1 and its dependent claims (i.e., claims 3-5). It is therefore respectfully requested that the anticipation rejection be withdrawn with respect to claims 1 and 3-5.

Claim 6 has also been amended to recite “wherein said memory buffer stores the data, and wherein said means performs said second de-interleaving as the stored data is read from said memory buffer”. Thus, for the same reasons as state above, claim 6 and its dependent claims (i.e., claims 7 and 8) are not anticipated by the Shiu patent. It is therefore respectfully requested that the anticipation rejection be withdrawn with respect to claims 6-8.

Claim 9 has been amended to recite “wherein said memory buffer stores the data, and wherein said read/write unit performs said second de-interleaving as the stored data is read from said memory buffer”. Thus, for reasons similar to those stated above, claim 9 is not anticipated by the Shiu patent. It is therefore respectfully requested that the anticipation rejection be withdrawn with respect to claim 9.

Claim 11 has been amended to recite “performing a second de-interleaving as the data is read from a memory buffer”. Thus, for reasons similar to those stated above, claim 11 and its dependent claim (i.e., claim 12) are not anticipated by the Shiu patent. It is therefore respectfully requested that the anticipation rejection be withdrawn with respect to claims 11 and 12.

Claim 13 has been amended to recite “storing the demodulated data in a memory buffer; reading said data from said memory buffer according to a second de-interleaving pattern”. Thus, for reasons similar to those stated above, claim 13 and its dependent claim (i.e., claim 14) are not anticipated by the Shiu patent. It is therefore respectfully requested that the anticipation rejection be withdrawn with respect to claims 13 and 14.

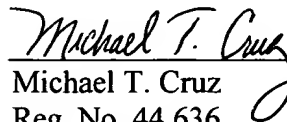
Claim 15 has been amended to recite “wherein said memory buffer stores the data, and wherein said means performs said second de-interleaving as the stored data is read from said memory buffer”. Thus, for the same reasons as state above, claim 15 and its dependent claim (i.e., claim 17) are not anticipated by the Shiu patent. It is therefore respectfully requested that the anticipation rejection be withdrawn with respect to claims 15 and 17.

In view of at least the foregoing, it is respectfully submitted that the pending claims 1, 3-9, 11-15 and 17 are in condition for allowance. Should anything remain in order to place the present application in condition for allowance, the Examiner is kindly invited to contact the undersigned at the below-listed telephone number.

Please charge any required fees not paid herewith or credit any overpayment to the Deposit Account of McAndrews, Held & Malloy, Ltd., Account No. 13-0017.

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Respectfully submitted,



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